

Amendment to the Abstract:

The Abstract has been amended. A revised Abstract is attached.

ABSTRACT

A power amplifier comprises an amplifier of amplifying a high-frequency signal modulated by an input signal, ~~an~~ An inverted envelope ~~generating~~ generates circuit generating an inverted envelope signal by using the input signal or the high-frequency signal, ~~the~~ The inverted envelope signal having an inverted envelope, ~~a~~ A variable gain variable amplifier ~~adjusting~~ adjusts the amplitude of the inverted envelope signal, and a variable delay circuit ~~adjusting~~ adjusts a phase of the inverted envelope signal, ~~And~~ a control circuit ~~outputting~~ outputs a control signal to the variable gain variable-amplifier and the variable delay circuit based on information about distortion or a signal level of the amplified high-frequency signal. The inverted envelope signal having been adjusted in amplitude or phase is injected to the high-frequency signal or the amplified high-frequency signal, ~~and~~ the control circuit controls the variable gain variable-amplifier or the variable delay circuit so that distortion or a signal level of the amplified high-frequency signal is minimum.

Respectfully submitted,


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Attachment: Abstract

Dated: July 29, 2003

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Kathleen Libby

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ABSTRACT

A power amplifier comprises an amplifier amplifying a high-frequency signal modulated by an input signal. An inverted envelope generates circuit generating an inverted envelope signal by using the input signal or the high-frequency signal. The inverted envelope signal having an inverted envelope. A variable gain amplifier adjusts the amplitude of the inverted envelope signal, and a variable delay circuit adjusts a phase of the inverted envelope signal. A control circuit outputs a control signal to the variable gain amplifier and the variable delay circuit based on information about distortion or a signal level of the amplified high-frequency signal. The inverted envelope signal having been adjusted in amplitude or phase is injected to the high-frequency signal or the amplified high-frequency signal. The control circuit controls the variable gain amplifier or the variable delay circuit so that distortion or a signal level of the amplified high-frequency signal is minimum.